

FIG. 1

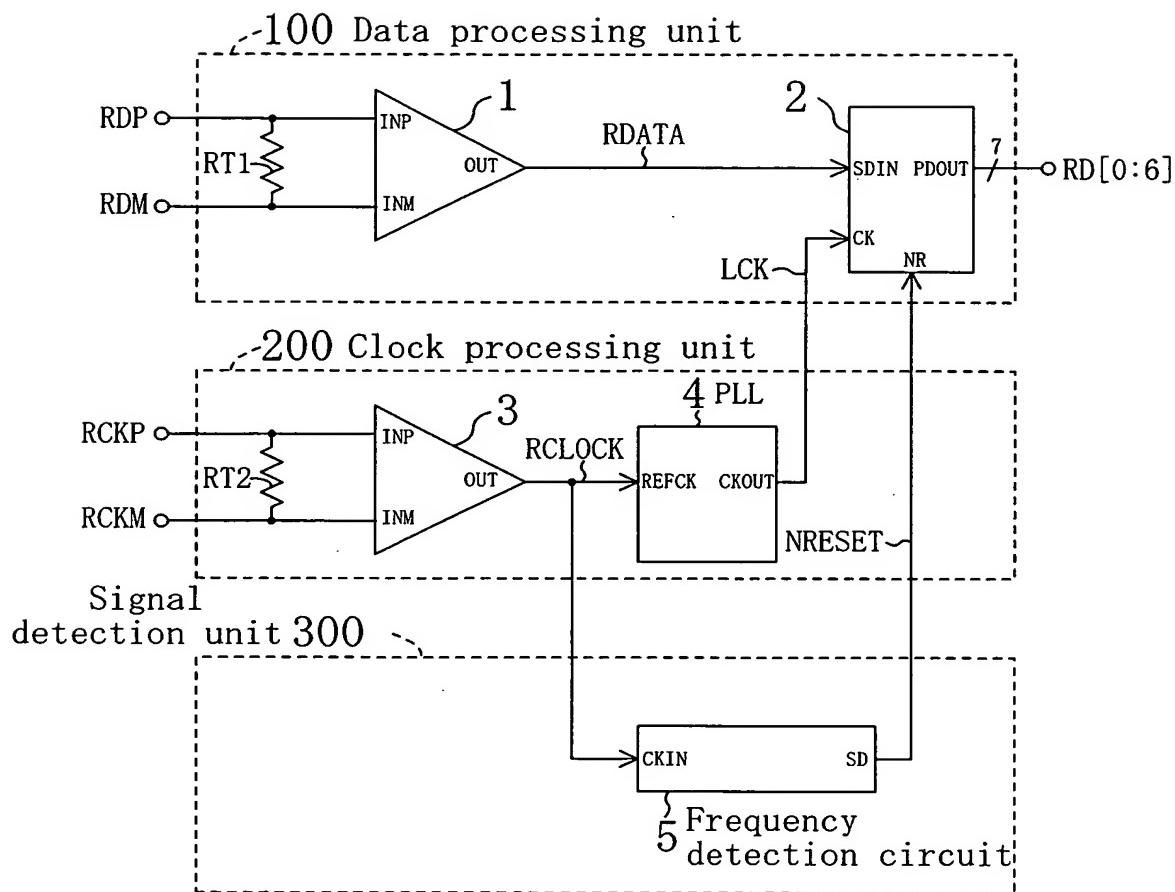


FIG. 2

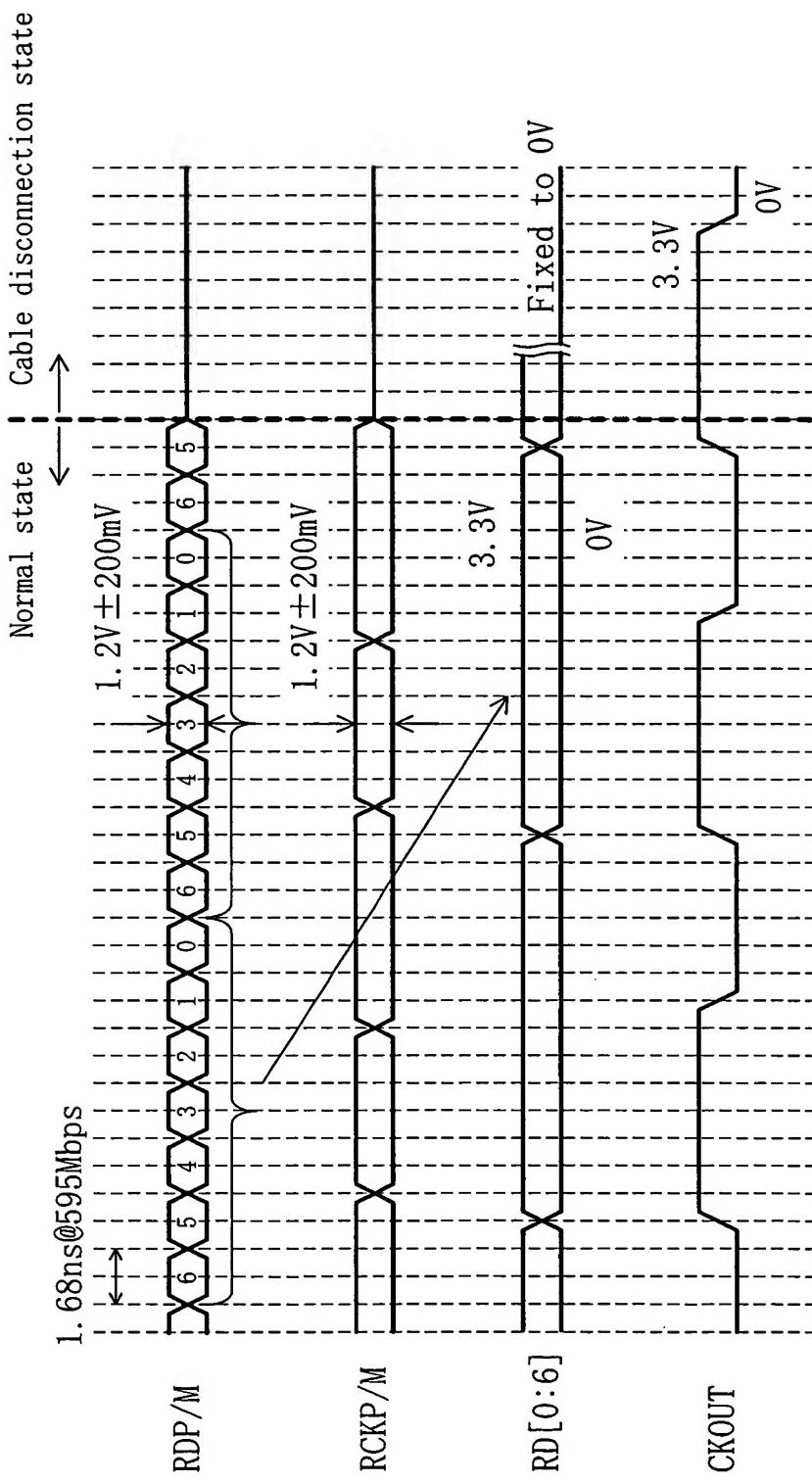


FIG. 3

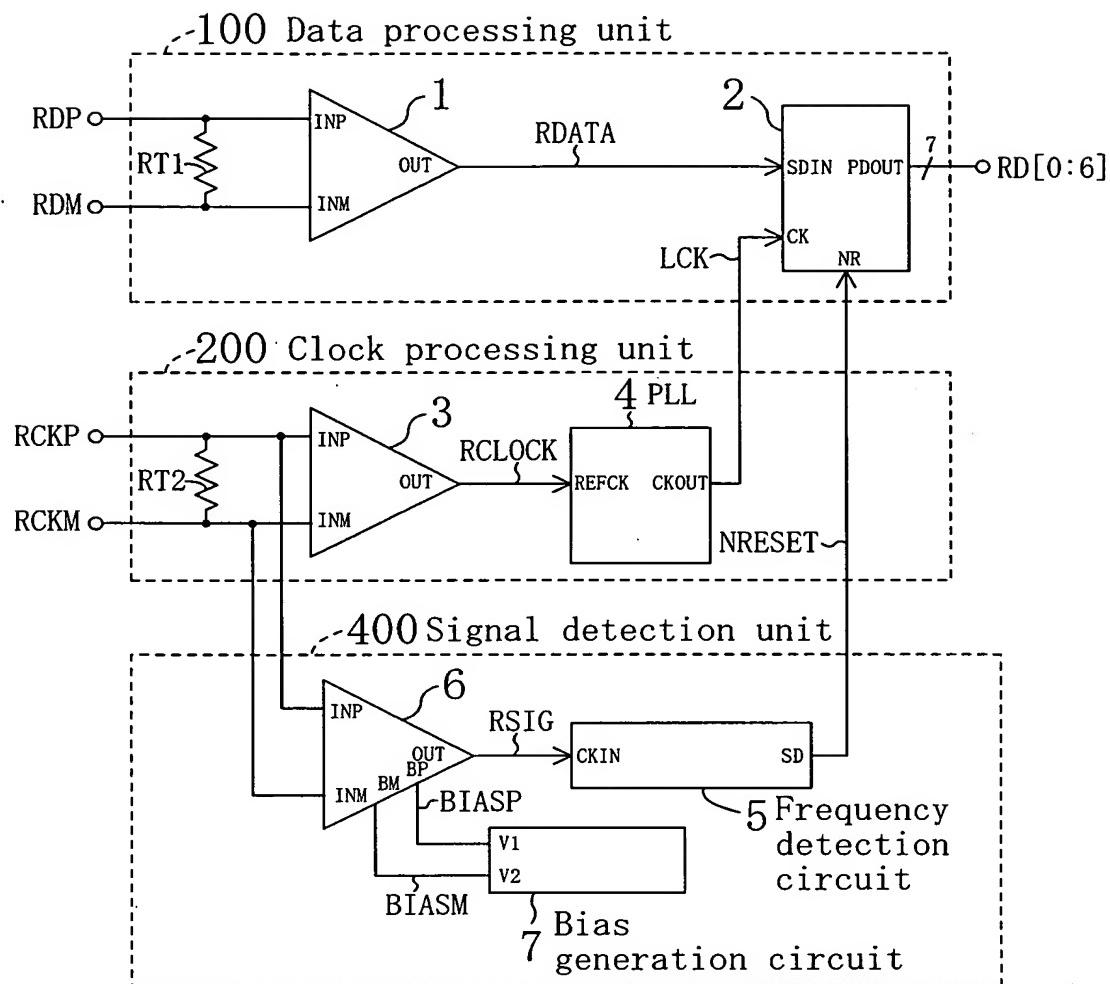


FIG. 4

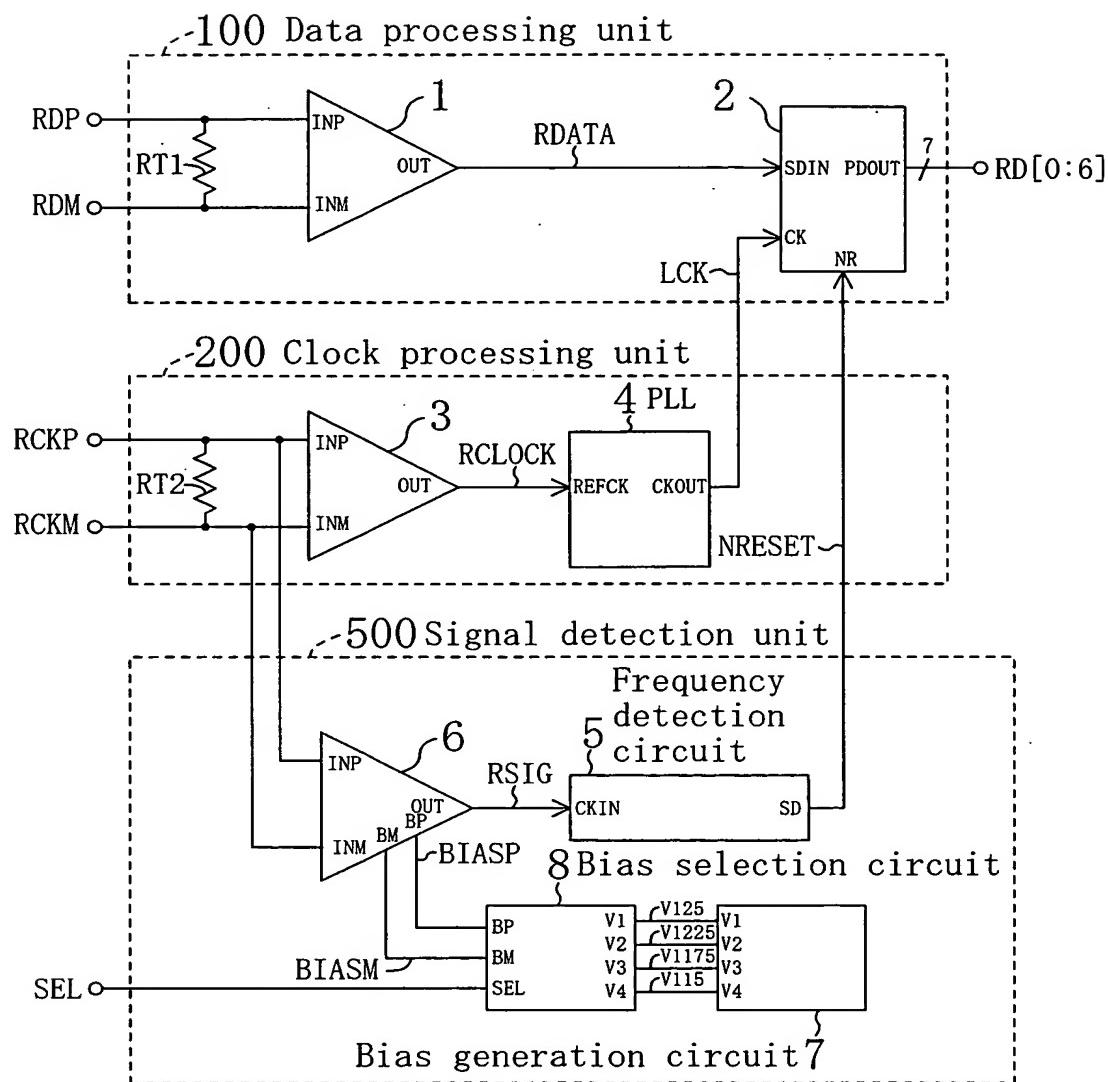


FIG. 5
100 Data processing unit
200 Clock processing unit
500 Signal detection unit
600 Resistor reference unit
700 Bias generation circuit

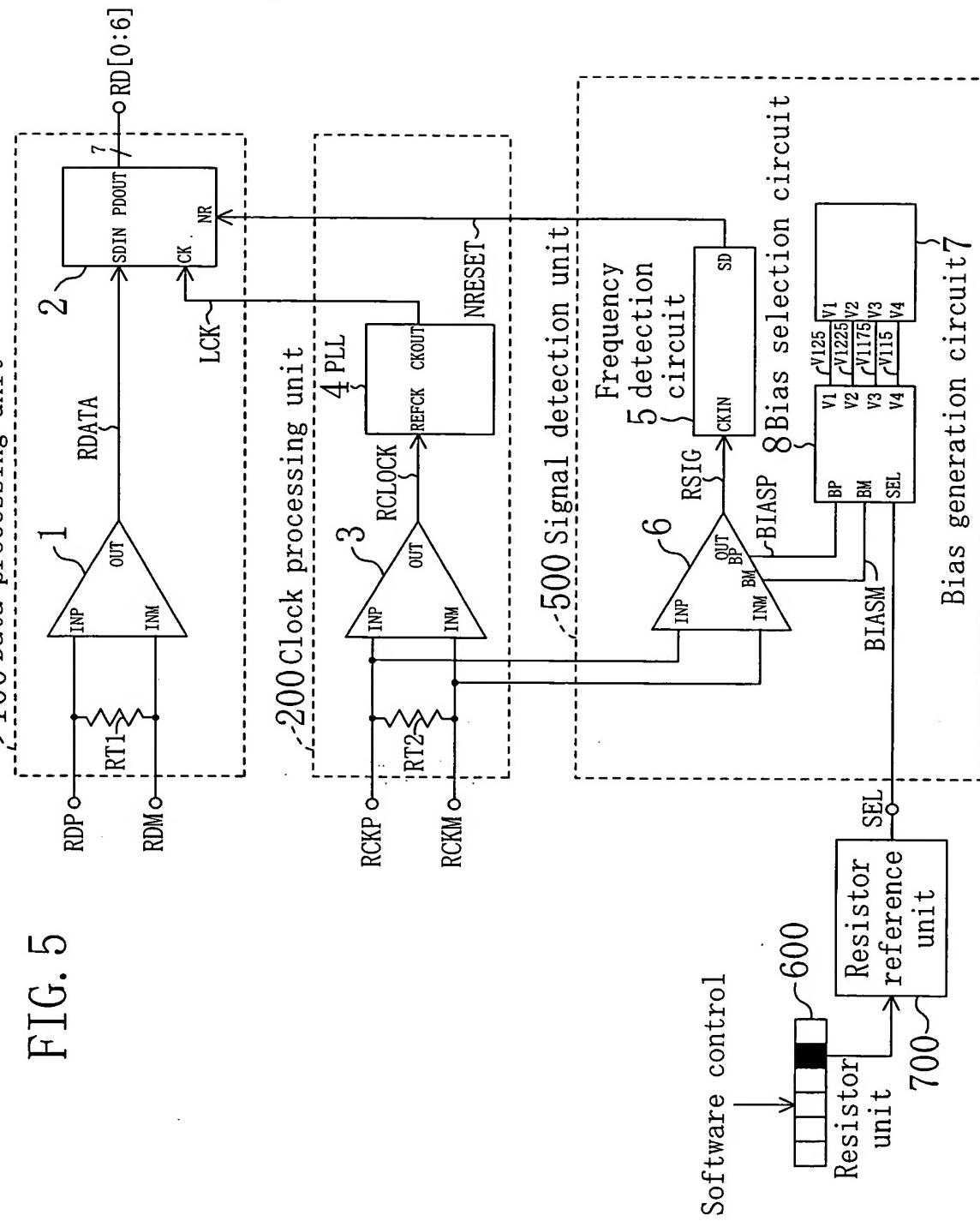


FIG. 6
PRIOR ART

